Abstract

There is provided an information processor including a central processing unit that includes an instruction execution module and has a normal mode for operating the instruction execution module and an execution halt mode for halting the instruction execution module. The information processor comprises a voltage controlling module for causing the instruction execution module to execute a voltage reduction instruction for placing the central processing unit into a low-voltage operation mode in which the operating voltage of the central processing unit is lowered from the operating voltage in the normal mode when the central processing unit switches from the normal mode to the execution halt mode; and a mode controlling module for placing the central processing unit into a low-voltage halt mode in which the instruction execution module is halted under the operating voltage for the low-voltage operation mode when the voltage controlling module places the central processing unit into the low-voltage operation mode.